

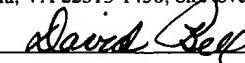


COFC

PATENT
Docket No. SPLX.P0005

CERTIFICATE OF MAILING BY "FIRST CLASS MAIL"

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 15, 2005.


David L. Bell

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.: 09/739,582

Patent No.: 6,870,255 B1

Filing Date: 12/15/2000

For: INTEGRATED CIRCUIT WIRING
ARCHITECTURES TO SUPPORT
INDEPENDENT DESIGNS

Examiner: Chu, Chris C.

Group Art Unit: 2815

*Certificate
NOV 22 2005
of Correction*

TRANSMITTAL LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION

Attention Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached, please, find a Request for Certificate of Correction on PTO/SB/44 Form. There is no fee associated with this Request as the corrected errors are not the fault of the Applicant. However, in the unlikely event that the Patent Office determines that additional fee is required, Applicant authorizes the Assistant Commissioner to charge the cost of such fees to Deposit Account No. 50-1128 referencing SPLX.P0005.

NOV 23 2005

Respectfully submitted,

Dated: November 15, 2005

By:

David L. Bell
David L. Bell
Registration No. 45,444

Stattler, Johansen & Adeli LLP
1875 Century Park East, Suite 1050
Los Angeles, California 90067

Telephone: (310) 785-0140 ext.305
Facsimile: (650) 785-9558

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,870,255 B1

APPLICATION NO.: 09/739,582

ISSUE DATE: Mar. 22, 2005

INVENTOR(S) : Steven Teig, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the CLAIMS, Column 14, Line 60, Claim 1, the phrase "wherein said pre-designed circuit block is a less than the" should read --wherein said pre-designed circuit block is less than the--

In the CLAIMS, Column 16, Line 17, Claim 12, the phrase "re-designed circuit block is independent of a layout for said" should read --pre-designed circuit block is independent of a layout for said--

In the CLAIMS, Column 16, Line 49, Claim 20, the phrase "percent of the entire area of said first metal layer group" should read --percent of the entire area of said metal layer in said first metal layer group.--

MAILING ADDRESS OF SENDER: STATTLER, JOHANSEN & ADELI

1875 Century Park East, Suite 1050
Los Angeles, CA 90067-2570

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

NOV 23 2005